

# mass integrated

global part distribution • product lifecycle partner  
excess inventory management • value-ad service provider



Data sheet provided by Mass Integrated

[massintegrated.com](http://massintegrated.com) • [displayalliance.com](http://displayalliance.com)

Please call +1.978.465.6190  
Email: [sales@massintegrated.com](mailto:sales@massintegrated.com)



ISO 9001: 2008

Mass Integrated complies with all DMCA takedown notices. Please send a formal DMCA Takedown Notice to Mass Integrated Systems, Inc. • 18 Henry Graf Jr. Road, Unit 1 • Newburyport, MA 01950.



# TABLE OF CONTENTS

A

1.APPLICATIONS..... 3

2.PRODUCT NAME AND MODEL NAME

2-1.Product Name ..... 3

2-2.Model Name ..... 3

3.OVERVIEW ..... 3

4.CONFIGURATION..... 3

5.MECHANICAL SPECIFICATIONS ..... 4

B

6.ABSOLUTE MAXIMUM RATINGS ..... 5

7.RECOMMENDED OPERATING CONDITIONS..... 5

8.ELECTRICAL SPECIFICATIONS ..... 6

9.OPTICAL SPECIFICATIONS..... 8

10.INTERFACE SPECIFICATIONS

10-1.Signal descriptions ..... 12

10-2.Color Data Assignment ..... 15

10-3.Input Signal Timing ..... 16

C

10-4.Correspondence between Data and Display Position ..... 18

10-5.Power Supply Sequence ..... 18

11.BACKLIGHT SPECIFICATIONS

11-1.Pin Configuration for Backlight..... 19

11-2.CCFL ..... 20

11-3.Life..... 20

12.APPEARANCE SPECIFICATIONS

12-1.Appearance ..... 21

12-2.Dot defects ..... 21

D

13.ENVIRONMENTAL SPECIFICATIONS ..... 23

14.INDICATIONS ..... 24

15.PACKAGING ..... 24

16.WARRANTY ..... 25

17.PRECAUTIONS ..... 25

18. MISCELLANEOUS..... 27

OUTLINE DRAWING..... 28

A

B

C

D

E

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	2 /	

F

### 1. APPLICATIONS

This specification is applied to the 22.0 in. XGA-WIDE supported TFT-LCD module.

### 2. PRODUCT NAME AND MODEL NAME

2-1. Product Name : LCD Module

2-2. Model Name : FLC56XWC8V

### 3. OVERVIEW

This LCD module has a TFT active matrix type liquid crystal panel 1280×768 pixels, and diagonal size of 56cm (22.0-inch). This LCD has a LVDS interface and can display 16,777,216 colors.

The power supply of this LCD module is +12V DC single.

This module has the characteristics for applying TCO'99.

### 4. CONFIGURATION

This LCD module consists of a color TFT-LCD panel that is mounted with TFT driver ICs, a cold-cathode fluorescent tube back-light.

The inverter for the backlight is included.

Figure 4-1 shows a block diagram of this LCD module.

DATE	DOCUMENT CONTROL SECTION
------	--------------------------

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	3 /	

A

A

B

B

C

C

D

D

E

F

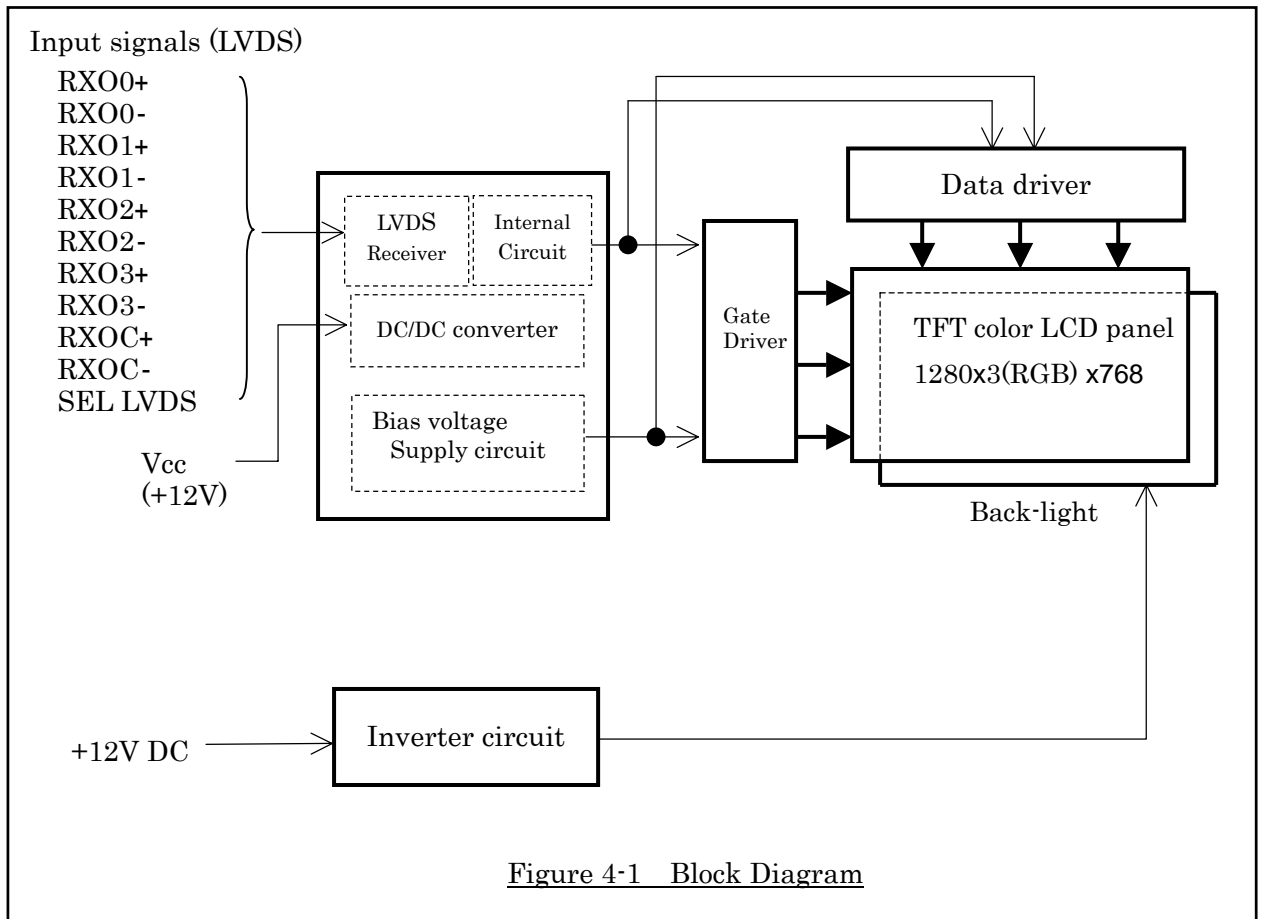


Figure 4-1 Block Diagram

### 5. MECHANICAL SPECIFICATIONS

Table 5-1 shows the mechanical specifications of this LCD module.

Table 5-1. Mechanical Specifications

Item	Specifications	Unit	Remark
Dimensions	508.0×316.0×33.8 (Typ.) (Exclude a inverter)	mm	*Direct lighting type backlight is used. *Include a inverter. *Inverter area thickness is (Typ.40.4mm) *For details on dimensions, See dimensional outline drawing. (At page 32 to 33 Fig.19-1,2,3)
Display Resolution	(1280×3) ×768	—	
Display Dot Area	480.0×288.0	mm	
Dot Pitch	(0.125×3) ×0.375	mm	
Aspect Ratio	1 : 1	—	
Weight	2,700 max	g	
FG-SG	Short circuit	—	

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	SHEET 4 /	

## 6. ABSOLUTE MAXIMUM RATINGS

Table 6-1 shows the absolute maximum rating of this LCD module.

Table 6-1. Absolute Maximum Ratings

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>CC</sub>	Ta=25°C	-0.3	—	14.0	V
	V <sub>INV</sub>	Ta=25°C	-0.3	—	14.0	V
Input Signal Voltage	V <sub>IN</sub>	Ta=25°C	-0.3	—	3.6	V
Control Voltage	V <sub>CNT</sub>	Ta=25°C	-0.3	—	V <sub>INV</sub>	V
Brightness Control Voltage	V <sub>VR4</sub>	Ta=25°C	0	—	4.0	V

## 7. RECOMMENDED OPERATING CONDITIONS

Table 7-1 shows the recommended operating conditions of this LCD module.

Table 7-1. Recommended Operating Conditions

Item	Symbol	MIN.	TYP.	MAX.	Unit	
Supply Voltage (Logic)	V <sub>CC</sub>	11.5	12.0	12.5	V	
Supply Voltage (Inverter)	V <sub>INV</sub>	10.8	12.0	13.2	V	
Ripple Voltage	V <sub>CC</sub>	V <sub>RP</sub>	—	—	0.1	V

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	5 /	

**8. ELECTRICAL SPECIFICATIONS**

Table 8-1 shows the electrical specifications of this LCD module.

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remark
Differential-input Voltage (High)	V <sub>IH</sub>	V <sub>CM</sub> =+1.2V	—	—	100	mV	
Differential-input Voltage (Low)	V <sub>IL</sub>		-100	—	—	MV	
Supply Current	I <sub>CC</sub>	V <sub>CC</sub> =+12.0±0.5V V <sub>SS</sub> =0V DCLK=65MHz 60Hz	—	(610)	(1200)	mA	*1
Supply Rush Current	I <sub>SCC</sub>		—	—	(4.7)	A	*2
Supply Rush Current Duration (1A excess)	T <sub>SCC</sub>		—	—	(0.2)	ms	
Supply Current	I <sub>INV</sub>	V <sub>INV</sub> =12.0V V <sub>VR</sub> =0V	—	(4.3)	T.B.D	A	*3
Brightness Control Voltage	V <sub>VR</sub>		0	—	3.5	V	
Lighting Frequency	f	V <sub>INV</sub> =12.0V, V <sub>VR</sub> =0V	—	T.B.D	—	KHz	
Lighting Fix Voltage	V <sub>ent</sub>		0	—	(0.8)	V	
Non-Lighting Fix Voltage	V <sub>ent</sub>		(2.1)	—	V <sub>INV</sub>		

(\*1) Typical current situation : vertical gray scale V<sub>CC</sub>=12.0V  
 Maximum current situation: pixel vertical line pattern. V<sub>CC</sub>=11.5V  
 Without rush current.

(\*2) These items prescribe the rush current for starting internal DC/DC.  
 Charging current to capacitors of V<sub>CC</sub> is not prescribed.

(\*3) External power supply for inverter shall have the current capacity more than (16)A of the supply current (I<sub>INV</sub>), otherwise the protective circuit of inverter (fuse) might not work.

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	SHEET 6 /	

Note1) Measurement circuit is based on Fig.8-1.

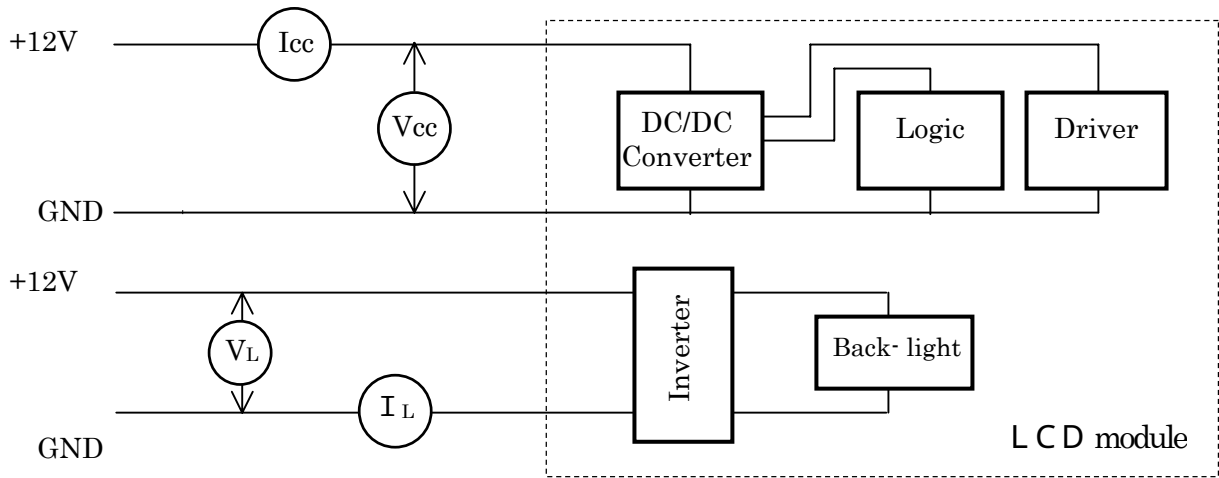


Fig.8-1. Measurement Circuit

Note 2) Equivalent Circuit is based on Fig.8-2 (a), (b).

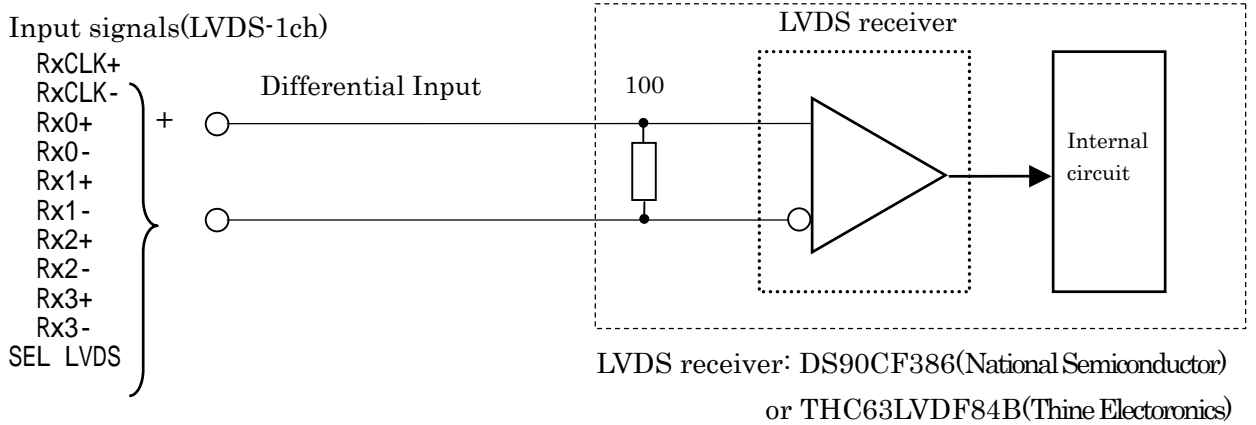
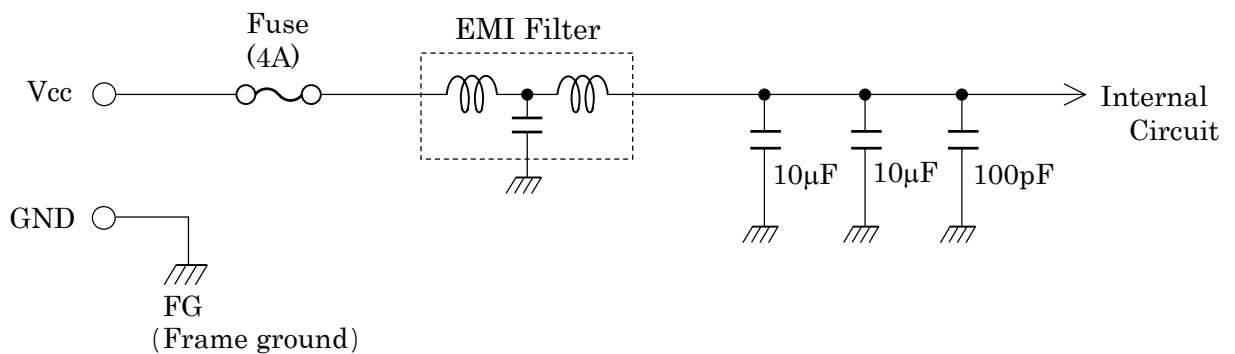


Fig.8-2 (a) Equivalent Circuit of Logic Signal Input



Fuse: KAB240240-4A (Matsuo Electric Co. LTD)  
EMI Filter: SGM45C1H222 (Sumitomo Metal Electronics Devices Inc)

Fig.8-2 (b) Equivalent Circuit of Power Supply

DOCUMENT CONTROL SECTION
DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.		7 /

## 9. OPTICAL SPECIFICATIONS

Table 9-1 shows the optical specifications of this LCD module.

Table 9-1. Optical Specifications

Ta=25°C

Item	Symbol	Condition	Specifications			Unit	Remark			
			MIN.	TYP.	MAX.			Note		
Visual Angle	Horizontal	L, R	CR 10	U, D = 0°	85	—	—	deg	(1)(2) (3)(5) (6)	
	Vertical	U, D		L, R = 0°	85	—	—	deg		
	All Direction				—	80	—	deg		
Contrast Ratio	CR	L, R, U, D = 0°		(400)	600	—	—	White/Black	(1)(2) (3)(5)	
Response Time (ON) (B W)	t <sub>on</sub>	L, R	= 0°	Ta=25°C	—	15	30	ms	(1) (4) (5)	
		U, D		Ta=0°C	—	50	100	ms		
Response Time (OFF) (W B)	t <sub>off</sub>	L, R	= 0°	Ta=25°C	—	10	25	ms		
		U, D		Ta=0°C	—	50	100	ms		
Response Time (ON or OFF) (All gray scale)	t <sub>avg</sub>	L, R	= 0°	Ta=25°C	—	15	—	ms	Average of Response Time	
		U, D		Ta=25°C	60±3Hz					
Brightness	I	L, R, U, D = 0° V <sub>CC</sub> =12.0V			400	500		cd/m <sup>2</sup>	White *1	(1)(5)
Brightness Uniformity	I	V <sub>INV</sub> =12.0V (At maximum Brightness)			70	—	—	%		(1)(5) (7)
Chromaticity	W	x				0.253	0.283	0.313		—
		y				0.267	0.297	0.327	—	
	R G B	(x, y)	Red				(x=0.637 y=0.353) Typ.			
			Green				(x=0.287 y=0.598) Typ.			
				Blue				(x=0.142 y=0.075) Typ.		
LCD Panel Type					TFT Color					
Display Mode					Normally Black					
Wide Viewing Angle Technology					MVA-Premium					
Optimum Viewing Angle					— (Symmetry)					(6)
Display Color					16,777,216 (8-bit color)					
Color of non-display area					Black					
Surface Treatment					Clear, Hard coating, Low reflection					

(\*1) Specified value is measured in 20~30 minutes after lighting on  
(The simplical module, Center point).

A required value may not be achieved on condition that LCD module is built in the cabinet because of its radiation.

(Note1) •CS-1000 (MINOLTA Co., Ltd.), BM-5A(Topcon) or equivalent luminance colorimeter should be used for the measurement. Field=2°, L=500mm

•The specified value of viewing angle, contrast, brightness, brightness uniformity and chromaticity are under the dark room condition (1lux or less).

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V				
						Tech Bes LCD-00175			CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION			FUJITSU DISPLAY TECHNOLOGIES CORPORATION		
DESIG.			CHECK		APPR.			8 /		

Note 1) Definition of Viewing Angle (1)

Based on Fig.9-2.

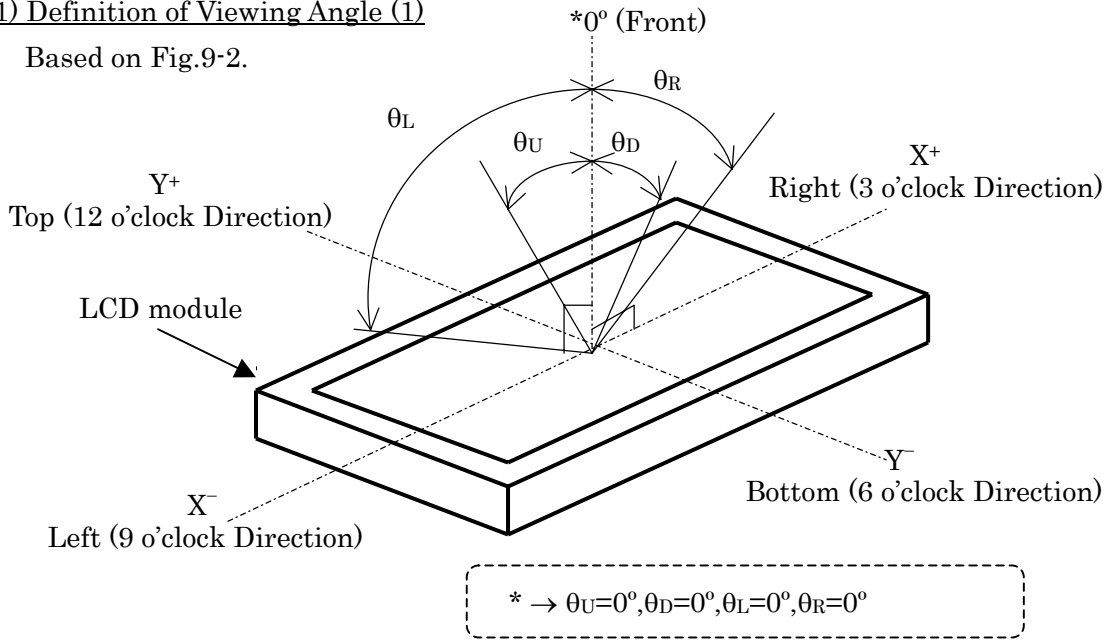


Fig.9-2. Definition of Viewing Angle (1)

Note 2) Definition of Viewing Angle (2)

Based on Fig.9-3.

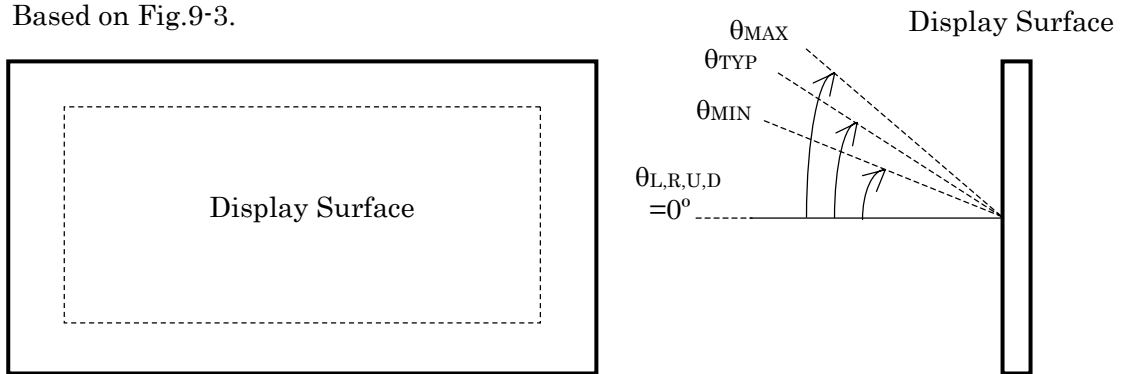


Fig.9-3. Definition of Viewing Angle (2)

Note 3) Definition of Contrast Ratio (CR)

Determined by Formula (1) based on Fig.9-4. Voltage-Brightness Characteristics.

$$= \frac{L_W \text{ (Brightness at white)}}{L_B \text{ (Brightness at black)}} \quad \text{----- (1)}$$

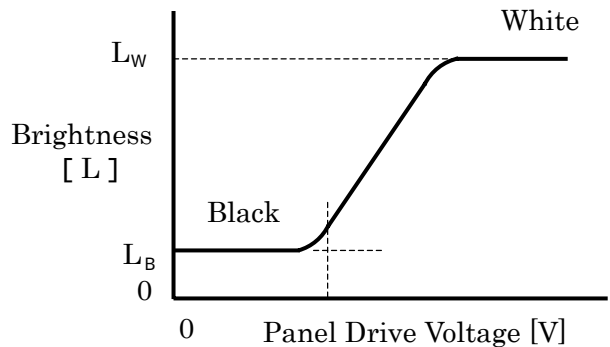


Fig.9-4. Voltage-Brightness Characteristics

DATE	DOCUMENT CONTROL SECTION			

						FLC56XWC8V		
						Tech Bes LCD-00175		CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION		9 /
DESIG.			CHECK		APPR.			

Note 4) Definition of Response Time

Based on Fig.9-5.

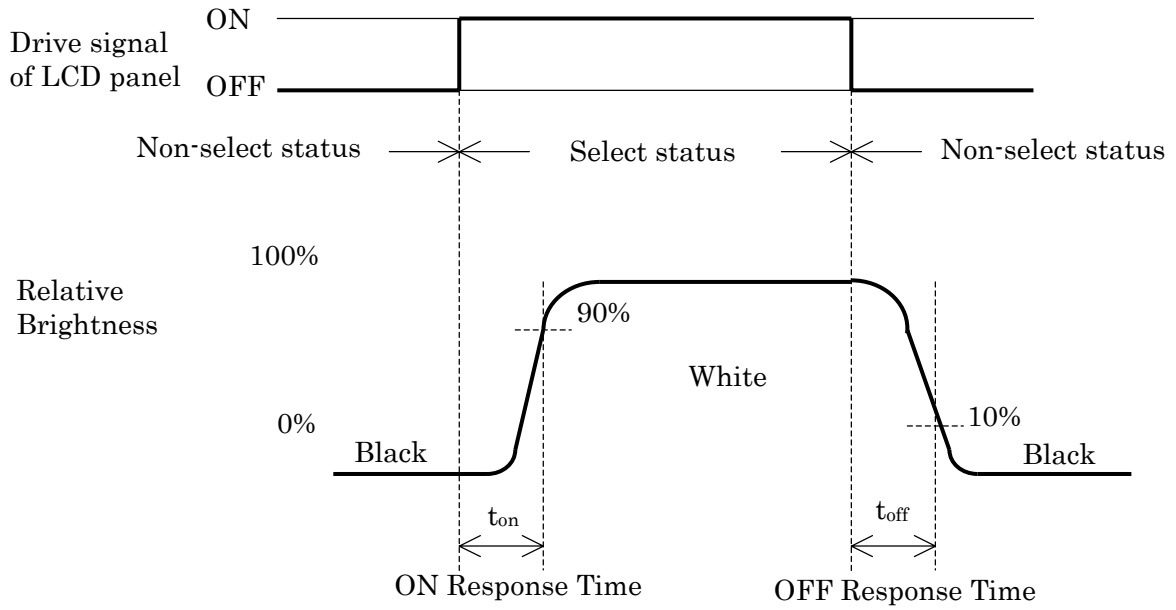


Fig.9-5. Definition of Response Time

Note 5) Contrast Ratio and Response Measurement System

Based on Fig.9-6.

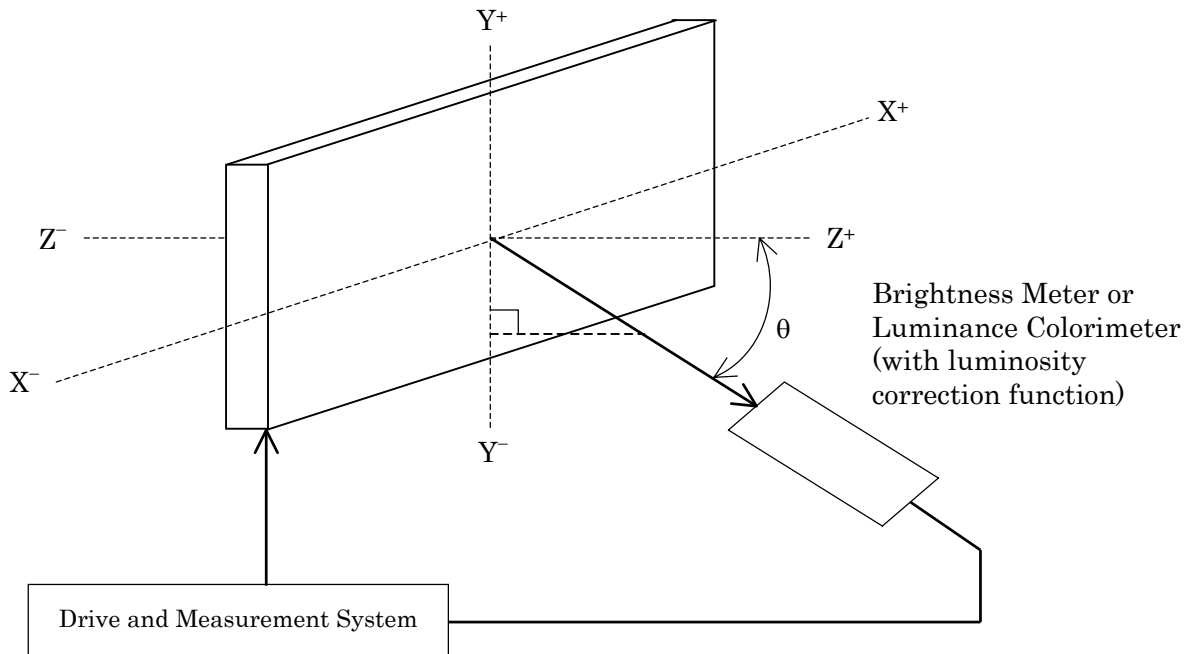


Fig.9-6. Contrast Ratio and Response Time Measurement System

DOCUMENT CONTROL SECTION
DATE

						FLC56XWC8V		
						Tech Bes LCD-00175		CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION		SHEET 10/
DESIG.			CHECK	APPR.				

Note 6) Definition of Optimum Viewing Angle

Based on Fig.9-7.

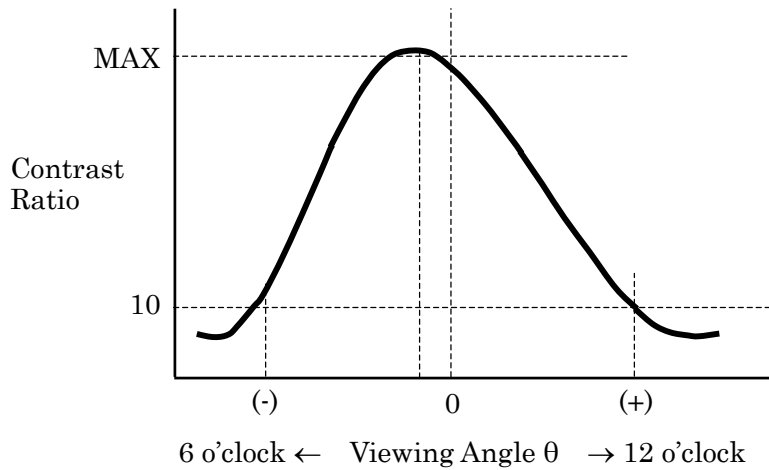


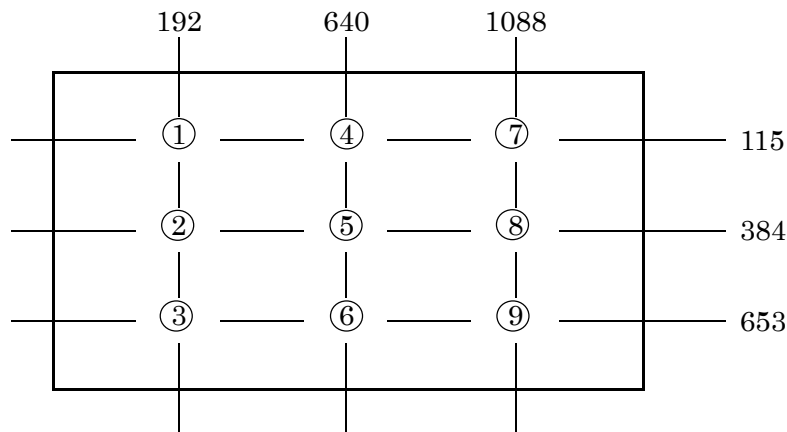
Fig.9-7. Definition of Viewing Angle

Note 7) Definition of Brightness Uniformity

Brightness uniformity is defined by the following formula.

Brightness (I1~I9) are measured at the following 9 points ( ① ~ ⑨ ) on the display area shown in Fig.9-8.

$$\text{Brightness Uniformity } (\Delta L) = \frac{|\text{Min. In}|}{|\text{Max. In}|} \times 100 (\%), n=1 \text{ to } 9$$



Note) Each measurement point ( ① ~ ⑨ ) defines the center spot of Brightness Meter view. The tolerance of measurement position is  $\pm 3\text{mm}$ .

Fig.9-8. Measurement Points

DOCUMENT CONTROL SECTION

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		11 /

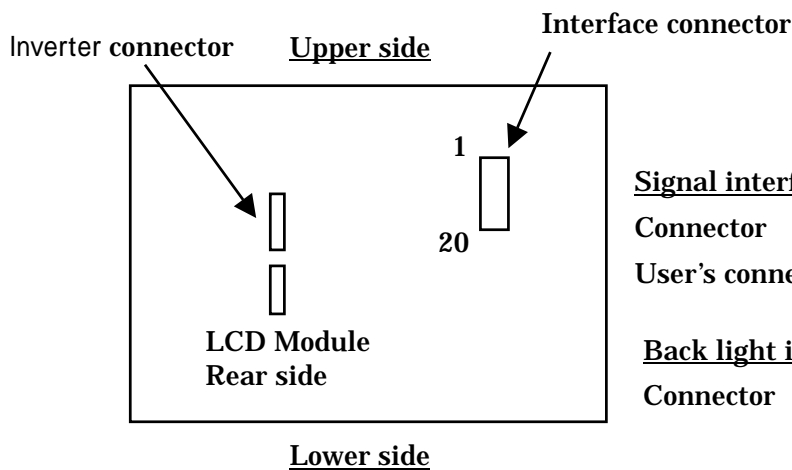
### 10. INTERFACE SPECIFICATIONS

#### 10-1-1. Signal descriptions

Table 10-1 shows the description and configuration of Interface signals (CN1).

Table 10-1-1. Interface signals (CN1)

Pin	Symbol	I/O	Function
1	VDD	-	+12V Power supply
2	VDD	-	+12V Power supply
3	GND	-	Ground
4	GND	-	Ground
5	RX0-	↓	LVDS Receiver Signal(-)
6	RX0+	↓	LVDS Receiver Signal(+)
7	GND	-	Ground
8	RX1-	↓	LVDS Receiver Signal(-)
9	RX1+	↓	LVDS Receiver Signal(+)
10	GND	-	Ground
11	RX2-	↓	LVDS Receiver Signal(-)
12	RX2+	↓	LVDS Receiver Signal(+)
13	GND	-	Ground
14	RXCLK-	↓	LVDS Receiver Clock Signal(-)
15	RXCLK+	↓	LVDS Receiver Clock Signal(+)
16	GND	-	Ground
17	RX3-	↓	LVDS Receiver Signal(-)
18	RX3+	↓	LVDS Receiver Signal(+)
19	GND	-	Ground
20	SEL LVDS	I	Select LVDS data order Hi : table 10-1-2 Open or Low : table 10-1-3



#### Signal interface

Connector : D14H-20P-1.25H(HIROSE)

User's connector : DF14-20S-1.25 (HIROSE)

#### Back light interface

Connector : S10B-PH-SM3-TB (JST)

S6B-PH-SM3-TB (JST)

User's connector : PHR-12(JST)

PHR-6(JST)

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
03 20030526						Correct Fig.,Connector	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		
						12 /	

**10-1-2. LVDS Data Mapping 1**

Table 10-1-2 shows the LVDS data mapping 1.(SEL LVDS = High)

**Table 10-1-2. LVDS Data Mapping 1**

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R 0	TxOUT0- TxOUT0+	5 6	RX0- RX0+	27	RxOUT0	IR 0
52	TxIN1	R 1				29	RxOUT1	IR 1
54	TxIN2	R 2				30	RxOUT2	IR 2
55	TxIN3	R 3				32	RxOUT3	IR 3
56	TxIN4	R 4				33	RxOUT4	IR 4
2	TxIN5	R 7	TxOUT3- TxOUT3+	17 18	RX3- RX3+	34	RxOUT5	IR 7
3	TxIN6	R 5	TxOUT0- TxOUT0+	5 6	RX0- RX0+	35	RxOUT6	IR 5
4	TxIN7	G 0				37	RxOUT7	IG 0
6	TxIN8	G 1	TxOUT1- TxOUT1+	8 9	RX1- RX1+	38	RxOUT8	IG 1
7	TxIN9	G 2	39			RxOUT9	IG 2	
8	TxIN10	G 6	TxOUT3- TxOUT3+	17 18	RX3- RX3+	41	RxOUT10	IG 6
10	TxIN11	G 7				42	RxOUT11	IG 7
11	TxIN12	G 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	43	RxOUT12	IG 3
12	TxIN13	G 4				45	RxOUT13	IG 4
14	TxIN14	G 5				46	RxOUT14	IG 5
15	TxIN15	B 0				47	RxOUT15	IB 0
16	TxIN16	B 6	TxOUT3- TxOUT3+	17 18	RX3- RX3+	49	RxOUT16	IB 6
18	TxIN17	B 7				50	RxOUT17	IB 7
19	TxIN18	B 1	TxOUT1- TxOUT1+	8 9	RX1- RX1+	51	RxOUT18	IB 1
20	TxIN19	B 2	TxOUT2- TxOUT2+	11 12	RX2- RX2+	53	RxOUT19	IB 2
22	TxIN20	B 3				54	RxOUT20	IB 3
23	TxIN21	B 4				55	RxOUT21	IB 4
24	TxIN22	B 5				1	RxOUT22	IB 5
25	TxIN23	RESERVED	TxOUT3- TxOUT3+	17 18	RX3- RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2- TxOUT2+	11 12	RX2- RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	ENAB
50	TxIN27	R 6	TxOUT3- TxOUT3+	17 18	RX3- RX3+	7	RxOUT27	IR 6
31	TxCLKIN	DCLK	TxCLKOUT- TxCLKOUT+	14 15	RXCLK- RXCLK+	26	RxCLKOUT	DCLK

DOCUMENT CONTROL SECTION

DATE

						<b>FLC56XWC8V</b>		
						<b>Tech Bes LCD-00175</b>		CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION		13 /
DESIG.			CHECK		APPR.			

**10-1-3. LVDS Data Mapping 2**

Table 10-1-3 shows the LVDS data mapping 2.(SEL LVDS = Open or Low)

**Table 10-1-3. LVDS Data Mapping 2**

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R 2	TxOUT0- TxOUT0+	5 6	RX0- RX0+	27	RxOUT0	IR 2
52	TxIN1	R 3				29	RxOUT1	IR 3
54	TxIN2	R 4				30	RxOUT2	IR 4
55	TxIN3	R 5				32	RxOUT3	IR 5
56	TxIN4	R 6				33	RxOUT4	IR 6
2	TxIN5	R 1	TxOUT3- TxOUT3+	17 18	RX3- RX3+	34	RxOUT5	IR 1
3	TxIN6	R 7	TxOUT0- TxOUT0+	5 6	RX0- RX0+	35	RxOUT6	IR 7
4	TxIN7	G 2				37	RxOUT7	IG 2
6	TxIN8	G 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	38	RxOUT8	IG 3
7	TxIN9	G 4	39			RxOUT9	IG 4	
8	TxIN10	G 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	41	RxOUT10	IG 0
10	TxIN11	G 1				42	RxOUT11	IG 1
11	TxIN12	G 5	TxOUT1- TxOUT1+	8 9	RX1- RX1+	43	RxOUT12	IG 5
12	TxIN13	G 6				45	RxOUT13	IG 6
14	TxIN14	G 7				46	RxOUT14	IG 7
15	TxIN15	B 2				47	RxOUT15	IB 2
16	TxIN16	B 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	49	RxOUT16	IB 0
18	TxIN17	B 1				50	RxOUT17	IB 1
19	TxIN18	B 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	51	RxOUT18	IB 3
20	TxIN19	B 4	TxOUT2- TxOUT2+	11 12	RX2- RX2+	53	RxOUT19	IB 4
22	TxIN20	B 5				54	RxOUT20	IB 5
23	TxIN21	B 6				55	RxOUT21	IB 6
24	TxIN22	B 7				1	RxOUT22	IB 7
25	TxIN23	RESERVED	TxOUT3- TxOUT3+	17 18	RX3- RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2- TxOUT2+	11 12	RX2- RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	E N A B
50	TxIN27	R 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	7	RxOUT27	IR 0
31	TxCLKIN	DCLK	TxCLKOUT- TxCLKOUT+	14 15	RXCLK- RXCLK+	26	RxCLKOUT	D C L K

DOCUMENT CONTROL SECTION

DATE

						<b>FLC56XWC8V</b>		
						<b>Tech Bes LCD-00175</b>		CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION		14 /
DESIG.			CHECK		APPR.			

10-2. Color Data Assignment

Table 10-2 shows the color data assignment.

Table 10-2. Color Data Assignment

Color and Brightness		Input data ( 0 : Low level , 1 : High level )																							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	2	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Brighter	253	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↓	254	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Red	255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	↑	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Brighter	253	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	
	↓	254	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
Green	255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0		
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	↑	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Brighter	253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	1
	↓	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0
Blue	255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

Note.1) Definition of gray scale : Color (n) ---"n" indicates gray scale level.

The gray scale is brighter as the number is larger.

Note.2) Data; 1: High, 0: Low

Note.3) By inputting 8-bit data signal for each red, green and blue, this module can display 256 gray scale independently for each color.

Therefore, the module is able to display 16 million colors. Color data are 24 lines.

DOCUMENT CONTROL SECTION

DATE

FLC56XWC8V

Tech Bes LCD-00175

CUST.

EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	15 /
DESIG.			CHECK		APPR.		

**10-3. Input Signal Timing**

Table 10-3 and Fig.10-3 shows the input signal timing.

**Table 10-3. Timing Characteristics**

(T=0~50°C, Vcc=12±0.5V)

Item		Symbol	Min.	Typ.	Max.	Unit	Remark		
DCLK signal (Clock)	Period	Tc	(12.500)	15.382	(20.000)	ns	fc=1/Tc *1		
	Frequency	fc	(50.00)	65.01	(80.00)	MHz			
	Duty	Tch/Tc	45	50	55	%			
	High time	TclkH	5.0	—	—	ns			
	Low time	TclkL	5.0	—	—	ns			
	Rise time	Tclkr	—	—	5.0	ns			
	Fall time	Tclkf	—	—	5.0	ns			
DCLK-Data Timing	Setup time	Tset	4.5	—	—	ns			
	Hold time	Thold	6.5	—	—	ns			
Data-ENAB timing	Horizontal	Period	Th	(1340)	1344	(2000)	DCLK	*2,3	
		Frequency	fh	(46.6)	48.3	(55.5)	kHz		
		Display period	Thd	1280	1280	1280	DCLK		
	Vertical	Period	Tv	(776)	806	806	Th		16.67ms
		Frequency	1/Tv	—	60	—	Hz		
		Display period	Tvd	768	768	768	Th		*2,3
Data-ENAB timing	Tdn	0	0	0	DCLK	*4			

\*1) DCLK signal input must be valid while power supply is applied.

\*2) Display position is specified by the ENAB signal only.

· Horizontal display position is specified by the rise of ENAB signal. The data which is latched by the falling edge of 1st DCLK right after the rise of ENAB, is displayed on the left edge of the screen.

· Vertical display position is specified by the rise of ENAB after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of ENAB is displayed at the top line of screen.

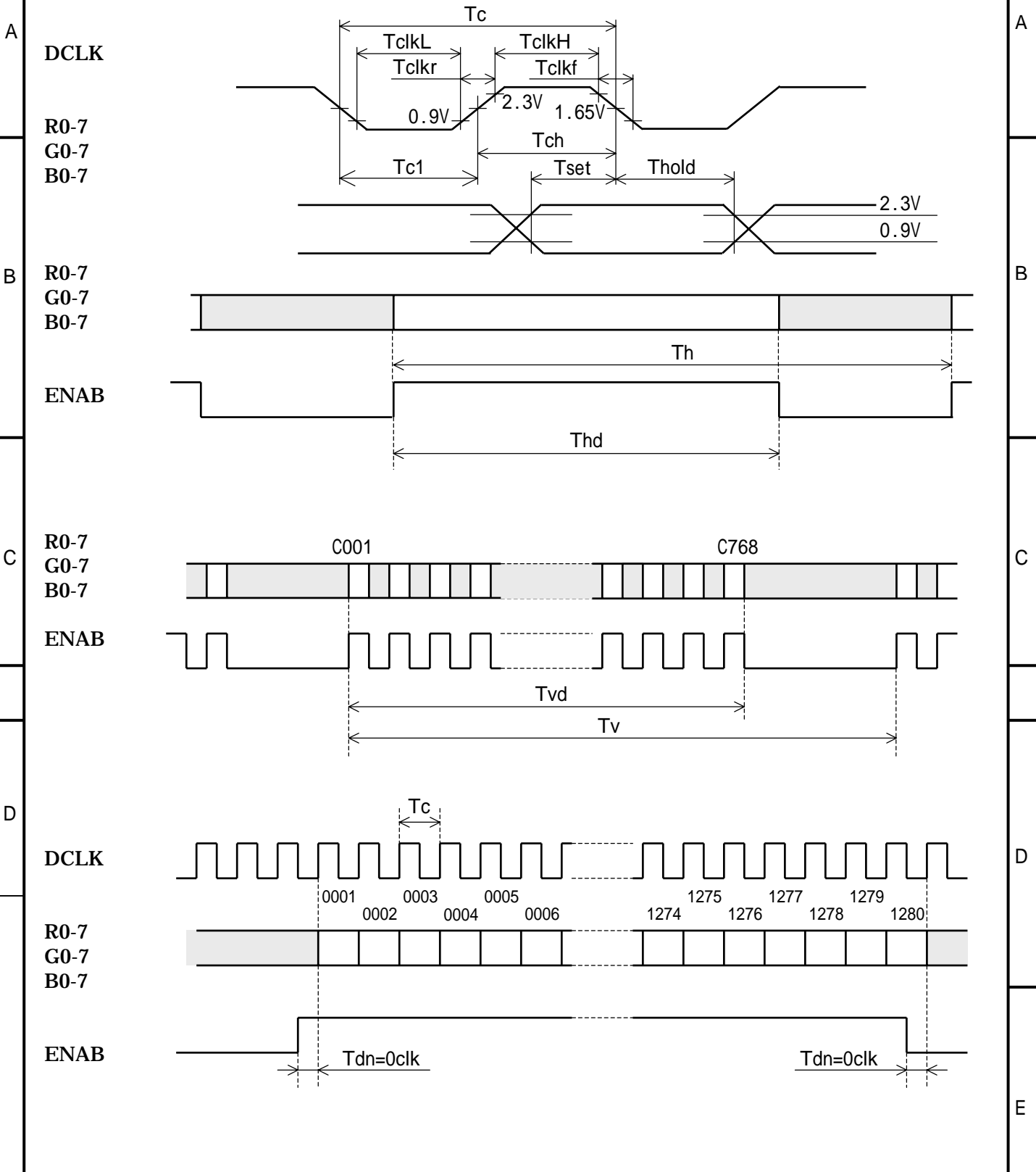
\*3) If a period of ENAB "High" is less than 1280 DCLK or less than 768 lines, the rest of the screen displays black.

\*4) The display position does not fit to the screen if a period of ENAB "High" and the effective data period do not synchronize with each other.

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V			
						Tech Bes LCD-00175		CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION		16 /
DESIG.			CHECK		APPR.				



**Fig.10-3. Input Signal Timing Chart**

DATE DOCUMENT CONTROL SECTION

						<b>FLC56XWC8V</b>	
						<b>Tech Bes LCD-00175</b>	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		
						17 /	

**10-4. Correspondence between Data and Display Position**

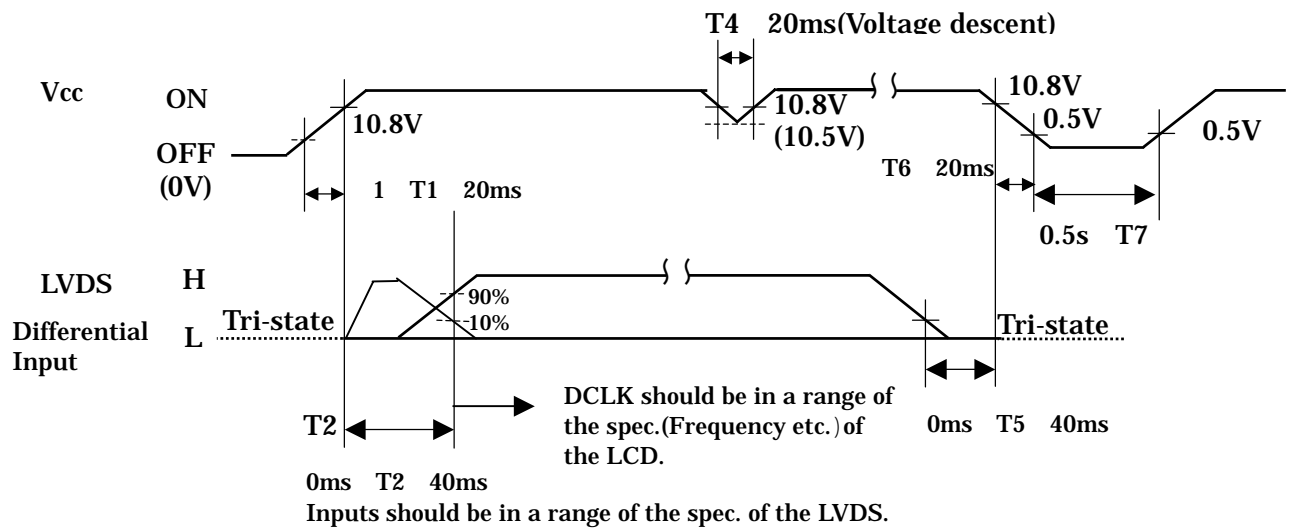
Fig.10-4 shows the Correspondence between Data and Display Position.

	S0001	S0002	S0003	S0004	S0005	S0006	S0007	S0008	-----	S3839	S3840
C001	R 0001	G 0001	B 0001	R 0002	G 0002	B 0002	R 0003	G 0003		G 1280	B 1280
C768	R 0001	G 0001	B 0001	R 0002	G 0002	B 0002	R 0003	G 0003		G 1280	B 1280

**Fig.10-4. Correspondence Data and Display Position**

**10-5. Power Supply Sequence**

The sequence of input signals and On/Off of the power supply of this LCD module should be in the specification shown in Fig.10-5 to prevent latch-up of the driver ICs and DC driving of the LCD panel.



**Fig.10-5. Power Supply Sequence**

DOCUMENT CONTROL SECTION

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		18 /

# 11. BACKLIGHT SPECIFICATIONS

## 11-1. Pin Configuration for Backlight

Table 11-1 shows the description and Pin assignment of the connectors (CN-2,3) for the Back-light of this LCD module.

**Table 11-1-1 Pin Assignment of CN-2**

Pin	Signal	Function
1	V <sub>INV</sub>	Power supply
2	V <sub>INV</sub>	Power supply
3	V <sub>INV</sub>	Power supply
4	V <sub>INV</sub>	Power supply
5	V <sub>INV</sub>	Power supply
6	GND	Ground (High voltage)
7	GND	Ground (High voltage)
8	GND	Ground (High voltage)
9	GND	Ground (High voltage)
10	GND	Ground (High voltage)

Connector : S10B-PH-SM3-TB

User's Connector: PHR-10

Supplier:  
Japan solderless terminal manufacturing Co.Ltd.

Note1) N.C terminal should keep open.

**Table 11-1-2 Pin Assignment of CN-3**

Pin	Signal	Function
1	V <sub>VR</sub>	Brightness control
2	N.C.	N.C.
3	N.C.	N.C.
4	V <sub>cnt</sub>	Lighting control
5	N.C.	N.C.
6	GND	Ground (low voltage)

Connector : S6B-PH-SM3-TB

User's Connector: PHR-6

Supplier:  
Japan solderless terminal manufacturing Co.Ltd.

Note1) N.C terminal should keep open.

Note2) V<sub>VR</sub> input:  
0V=Max. Brightness  
3.5V=Min. brightness

Note3) V<sub>cnt</sub> input:  
0V=BL turn on  
2.1V or more = BL turn off

Note4) Keep the circuit GND (CN-3 6pin) apart from high-voltage GND (CN-2 6-10pin). Otherwise, the screen may cause flickering.

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
03 20030526						Correct connector type	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		19 /

【FLC56XWC8V】

**11-2 CCFL**

Supplier: SANKEN ELECTRONICS Co.LTD. Part No.: T.B.D

A

A

**11-3 Life**

The life of the back-light is a minimum of 50,000 hours at the following conditions.

**(1) Working conditions**

Ambient temperature: 25 ± 5

Brightness control (Vvr): 0V

**(2) Definition of life**

Brightness becomes 50% or less than the minimum brightness value shown in Table 9-1.

The lamp no longer lights

Lamp being flashing or flickering.

B

B

C

C

D

D

DOCUMENT CONTROL SECTION

DATE

E

F

FLC56XWC8V

Tech Bes LCD-00175

CUST.

EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	20 /
DESIG.			CHECK		APPR.		

### 12. APPEARANCE SPECIFICATIONS

#### 12-1.Appearance

Table 12-1 shows the appearance specifications. In the case of another agreement about Specification arises, that agreement takes priority.

Table12-1 Appearance Specifications

Length: L[mm], Width: W[mm]

Allowable number of pieces: N, Average diameter: D [mm]

		Item	Judgment method and standard	Remarks	
1	Foreign Particle	White and Black points	D 0.5 N 10		
			0.5 < D N 0		
		Fiber	Dark line		W 0.1 N 10
			Bright points		L 12.0 N 10
2	Scratch	Scratch on polarizer film	D 0.3 Not count		
			0.3 < D 0.6 N 12		
3	Dent	Dent on polarizer film	D > 0.6 N 8		
			12.0 > L N 15		

#### Note

- Foreign particle and scratch that do not effect display image, such as foreign particle between glass and polarizer film out of the display area, scratch on metal bezel, backlight module or polarizer film out of the display area are not counted.
- Unwiped dirt out of the display area is not counted.
- These items are applied to the defects in the cell when backlight is on, and defects on the surface of the polarizer film at the display area.
- Must be observed the LCD screen from the normal direction unless specified. The distance between the LCD screen and the observing position should be 35cm or more. One 20W fluorescent lamp is used at 50cm above the worktable. At this time, the luminance at the vertical direction to the fluorescent lamp is 300 to 600 lux (reference value).
- Appearance Specifications are defined under the condition of frame frequency at 60Hz. (include Bright and Dark points specifications)

#### 12-2.Dot defects (Bright spots, Dark spots)

##### 12-2-1.Area to be inspected

- Inside display dot area (480.0 X 288.0mm)
- Display dot area means active area.
- One pixel consists of 3 dots (red, green and blue).

##### 12-2-2.Bright spots definition

(1) Bright spots are classified as follows. (based on brightness samples)

- Visible through 2% ND filter ..... High-bright spot ( R,G )
- Visible through 5% but invisible through 2% ND filter ..... Low-bright spot( R,G,B )
- Invisible through 5% ND filter ..... Not counted

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	21 /	

- (2) Tears, breaks, etc in color filter visible by the light passing through.
- Bigger than a half dot..... High bright spot
  - A half dot or smaller ..... Not counted
- (3) Tears, breaks, etc in black matrix visible by the light passing through.
- Diameter above 50 μ m ..... High-bright spot
  - Diameter of 50 μ m or smaller ..... Not counted

12-2-3. Number of bright spot standard

Item	Entire Screen	
	High-bright spots	High and Low Bright Spots
Brightness classification		
Number of defects	15 or less	22 or less

NOTES :

1. Display should be all black when bright spots are counted.
2. Number of two high Bright spots connections is up to 3.
3. Number of two low Bright spots connections is up to 12.
4. Number of three Bright spots connections and two high Bright spots vertical connections is 0.
5. Number of high Bright spots and low Bright spots connections is up to 5.

12-2-4. Distance between Bright spots

- Distance between Bright spots( not include B) ..... 15 mm or more
- Distance between Bright spots( include B) ..... 5 mm or more  
(Distance to the third defect should be 20mm or more)

12-2-5. Number of Dark spots standard

Item	Entire Screen
Number of defects	24 or less
Number of two dark spot connections	12 or less (Not include vertical, horizontal and diagonal connections)
Number of three dark spot connections	3 or less

NOTES :

1. Display should be all white when dark spot is counted.
2. Distance between defects is 5 mm or more.  
(Distance to the third defect should be 20mm or more)
3. If dark spot size is smaller than one dot, convert with following rule and sum up.
  - (a)  $A < 1/3$  : Not counted.
  - (b)  $1/3 \leq A < 2/3$  : Considered as 0.5 dot.
  - (c)  $2/3 \leq A$  : Considered as 1 dot.

(A= Dark spot size / dot size)

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V			
						Tech Bes LCD-00175		CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION		22 /
DESIG.			CHECK		APPR.				

### 13. ENVIRONMENTAL SPECIFICATIONS

Table 13-1 shows the environmental specifications.

**Table 13-1. Environmental Specifications**

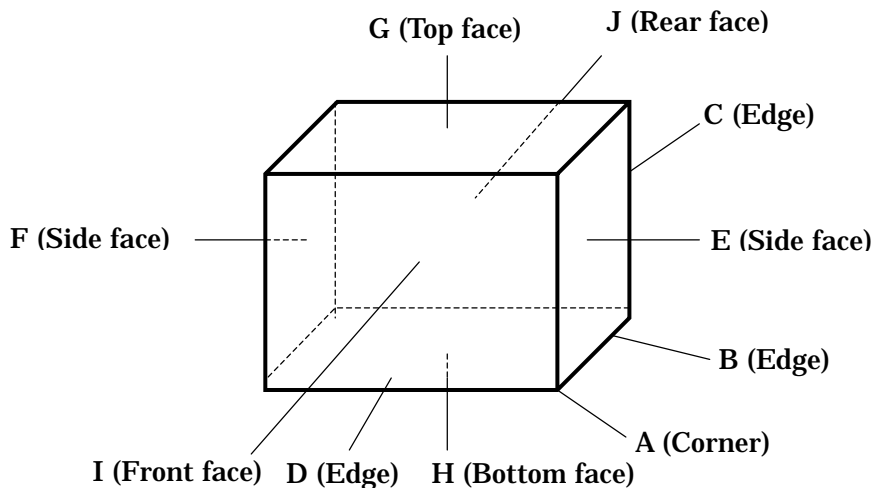
Item	Condition		Remark
Temperature	Operation	0~45°C	Note1
	Storage	-20~60°C	
Humidity	Operation	20~85%RH	Maximum wet-bulb temperature should not exceed 29°C. No condensation.
	Storage	5~85%RH	
Vibration	Non-operation	10~500Hz, 1 cycle/20minute, 2G, 1.5mm max, 2hour each X, Y and Z directions	For single module without package.(Note2)
Shock	Non-operation	30G, 6ms, 1time each ± X, ± Y and ± Z directions.	

Note1: Temperature on surface of LCD panel should be under (54) .

Note2: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged.

**Table 13-2. Shock Resistance Standard when Module is Packaged**

Dropping location	Dropping height	Count
A ~ J	60cm	1 time



**Fig.13-1. Direction to apply shock to package**

DOCUMENT CONTROL SECTION

DATE

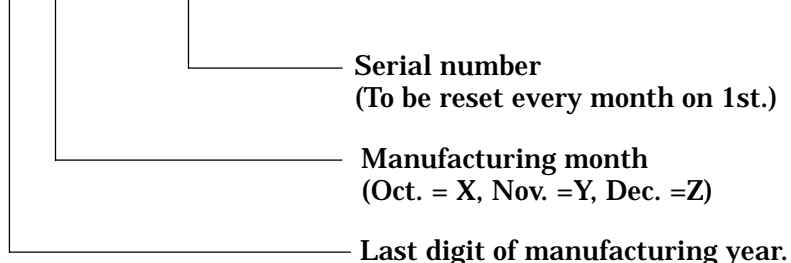
						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
DESIG.			CHECK		APPR.	23 /	

### 14. INDICATIONS

This module has the following indications.

- (1) Product name : LCD unit
- (2) Model number : FLC56XWC8V
- (3) Product drawing number: NA19026-C951

(4) Manufacturing number : 3 5 0 0 0 0 1



- (5) Version number : 01A (Example)  
 -1st 2 digits "01" means operational version.  
 -3rd alphabet means functional version.

(6) Country of origin : MADE IN JAPAN

(7) Company name : FUJITSU DISPLAY TECHNOLOGIES CORP.

(8) Caution when changing cold-cathode tubes and disposal method of them. (See Fig. 14-1)

· WHEN CHANGING COLD CATHODE FLUORESCENT LAMPS, FOLLOW OPERATING SPECIFICATIONS. ESPECIALLY BE CAREFUL ABOUT THE LAMP'S SIDE-EDGE.  
 · 蛍光管の交換は作業仕様書に従って行って下さい。特に蛍光管ホルダ側面のエッジに気をつけて下さい。

· THIS TFT COLOR LCD CONTAINS COLD CATHODE FLUORESCENT LAMPS. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR ITS DISPOSAL.  
 · 当該液晶ディスプレイユニットには蛍光管が組み込まれていますので、地方自治体の条例または規則に従って廃棄して下さい。

Fig.14-1

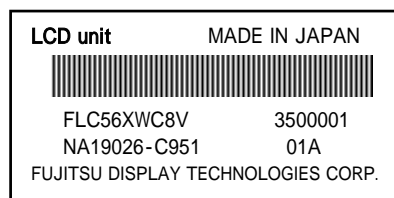


Fig.14-2 Product label (example)

### 15. PACKAGING

Separately specified in packaging specifications.

#### 15-1. Packaging specifications

- (1) 5 LCD modules / 1 package.
- (2) Weight: approximately (14) kg / 1 package.
- (3) Outline dimensions : (T.B.D) mm(W) × (T.B.D) (D) × (T.B.D) mm(H)

#### 15-2. Packaging method

Fig.15-2 (a),(b),(c),(d) show the packing method.

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V		
						Tech Bes LCD-00175		CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION			FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		24 /	

**16.WARRANTY**

The warranty period is one year after shipping. Products which fail during this period are repaired or replaced without charge, unless the failure is caused by user.

**17.PRECAUTIONS**

Adhere to the following precautions to properly use this LCD module.

**(1) Fail safe design**

LCD module has an inherent chance of failure. Customers must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.

**(2) Handling of LCD panel**

**Do not apply any strong mechanical shock to the LCD panel.**

Since the LCD panel is made of glass, excessive shock may damage the panel or cause a malfunction.

**Do not press hard on the LCD panel surface.**

In the LCD panel, the gap between two glass plates is kept precisely and uniformly to maintain display's characteristics and reliability. If this panel is pressed hard, the following troubles occurs.

- (a) Ununiformity of color
- (b) Orientation of liquid crystal becomes disordered

Problem (a) returns to normal after a while. Problem (b) returns to normal if power is shut off once then turned on again.

However these operations should be avoided to insure reliability.

**Do not scratch the polarizer film on the LCD panel surface.**

- Do not press or rub the display surface with a hard tool, pincet, etc.
- For handling, use cotton or conductive gloves so that the display surface is not stained.
- For If the display surface is stained by dust or dirt, clean it as follows with a soft cloth (deer skin, etc.)

[Dust] Wipe off with a soft cloth. (do not rub.)

[Dirt ] Wipe off lightly with a soft cloth after soaking in the clear water and squeezing hard out of water drops. Only if the dirt is hardly wiped off, use isopropyl alcohol or ethanol.

Be careful not to splash the water or the solvent and water penetrated between the polarizer and the LCD panel.

Do not use solvents such as ketone (acetone, etc.) and aromatics (xylene, toluene, etc.)

- If saliva or water drops are left for long time, it may deform partial deformation or discolored. Wipe off immediately in the same way as for dirt.
- Do not allow oil to adhere to the module, since the cleaning of oil is difficult.

**Do not place or contact objects on the display surface for a long period of time.**

That's because this may make some parts of the LCD module distorted and the display quality may decline.

DOCUMENT CONTROL SECTION

DATE

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		25 /

**(3) Handling of LCD module**

**Assemble the module into user's system in a dust free environment.**

If conductive foreign matter adheres to the module, failures may occur.

**Take anti-static measures for assembling the module.**

Since the LCD module contains CMOS-ICs, the following considerations are necessary.

- For assembling the module, operator should be grounded and wear cotton or conductive gloves.
- Floor of work area and work table to assemble the LCD module should be covered with electrostatic shielding in order to discharge static electricity via an earth wire.
- If necessary, ground operation tools (soldering iron, radio pliers, pincet, etc.).
- Do not take the module out of the conductive bag until the time when the module is assembled.
- Assemble the module under low humidity (50%RH or less).

**Do not pull the connecting cable on the rear face of the LCD module strongly.**

**Do not disassemble or remodel the LCD module.**

If this LCD module is disassembled or remodeled, it may have some trouble, or the display quality and reliability may not be assured.

**(4) Precautions for operating the LCD module**

**Adhere to the specified power supply sequence.**

If not followed, the CMOS-IC may cause a latch-up, or the DC voltage may be applied the liquid crystal, and a failure or serious deterioration in display quality may occur.

**Do not operate the LCD module when condensation is present.**

If the LCD module is operated when condensation is on the terminals of the LCD panel, the terminals cause electrochemical reaction, and may reach disconnection. Condensation easily occurs especially when the module is moved from a cold environment to a warm environment.

**Trouble that occurs when the LCD module is used at not recommended temperature.**

- Operation at high temperature(>60°C) :Display colors shift to blue.
- Storage at high temperature (>60°C) :The polarizer film deteriorates and contrast decreases.
- Operation at low temperature(<0°C) :The response speed decreases considerably.
- Storage at low temperature (<-20°C) :The liquid crystal may solidify and become damaged.

**Always input the control signals at the correct timing.**

If control signals (DCLK, or ENAB) are not input, or if the timing is out of the specified timing, DC voltage may be applied to the liquid crystal and, as a result, cause image sticking or deterioration of contrast.

**(5) Precautions on designing module mounting**

**Do not press the display surface and bottom face of the LCD module.**

Display quality or reliability may be deteriorated if the installation of the LCD module is inappropriate and, as a result, excessive pressure is applied to the surface of LCD screen. Brightness uniformity or the reliability of CCFL may decrease if the pressure is applied to the backlight module.

DATE DOCUMENT CONTROL SECTION

						FLC56XWC8V	
						Tech Bes LCD-00175	
						CUST.	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.		26 /

**Consider the module mounting design, so that twisting and bending do not occur to the LCD module.**

Excessive twisting and bending may damage display quality and reliability.

**The power cable length between the LCD module and inverter should not be extended.**

Otherwise the backlight may not light or flickering may occur.

**Do not make the power cable of the backlight clung to a metal plate, etc.**

Backlight frequency current for backlight driving may leak to the metal and desired brightness may not be assured.

**When Mounting LCD module with M3 screw(x4) should be screw up under 30N-cm torque.**

**(6) Storage method**

**Do not store the LCD module in an atmosphere of organic solvent or corrosive gas.**

In an organic solvent atmosphere, the polarizer film discolors and display quality deteriorates.

In a corrosive gas environment, various problems may occur.

**Store the LCD module in a Fujitsu package.**

At storing, Fujitsu packages can be stacked up to (T.B.D) boxes.

The LCD module is in an anti-static bag. Keep the module in that status.

**It is recommended that the storage environment should be humidity controlled, cool and dark.**

Recommended storage environment

- Place :Dark (avoid direct sunlight)
- Temperature : 10~35°C
- Humidity : 50~60%RH

Note) If the module is left in an environment of 60 or more for a long period of time, optical characteristics may deteriorate.

**(7) Storage method**

**If the LCD panel is damaged, do not inhale or allow the liquid crystal to enter the mouth**

If the liquid crystal contacts the body or cloths, wash it off with soap immediately. Follow precautions for regular electronic components.

**Solder flux on the printed circuit board is harmless to the quality and reliability of LCD module.**

Fujitsu is practicing non-wash technology on module assembly process.

**18. MISCELLANEOUS**

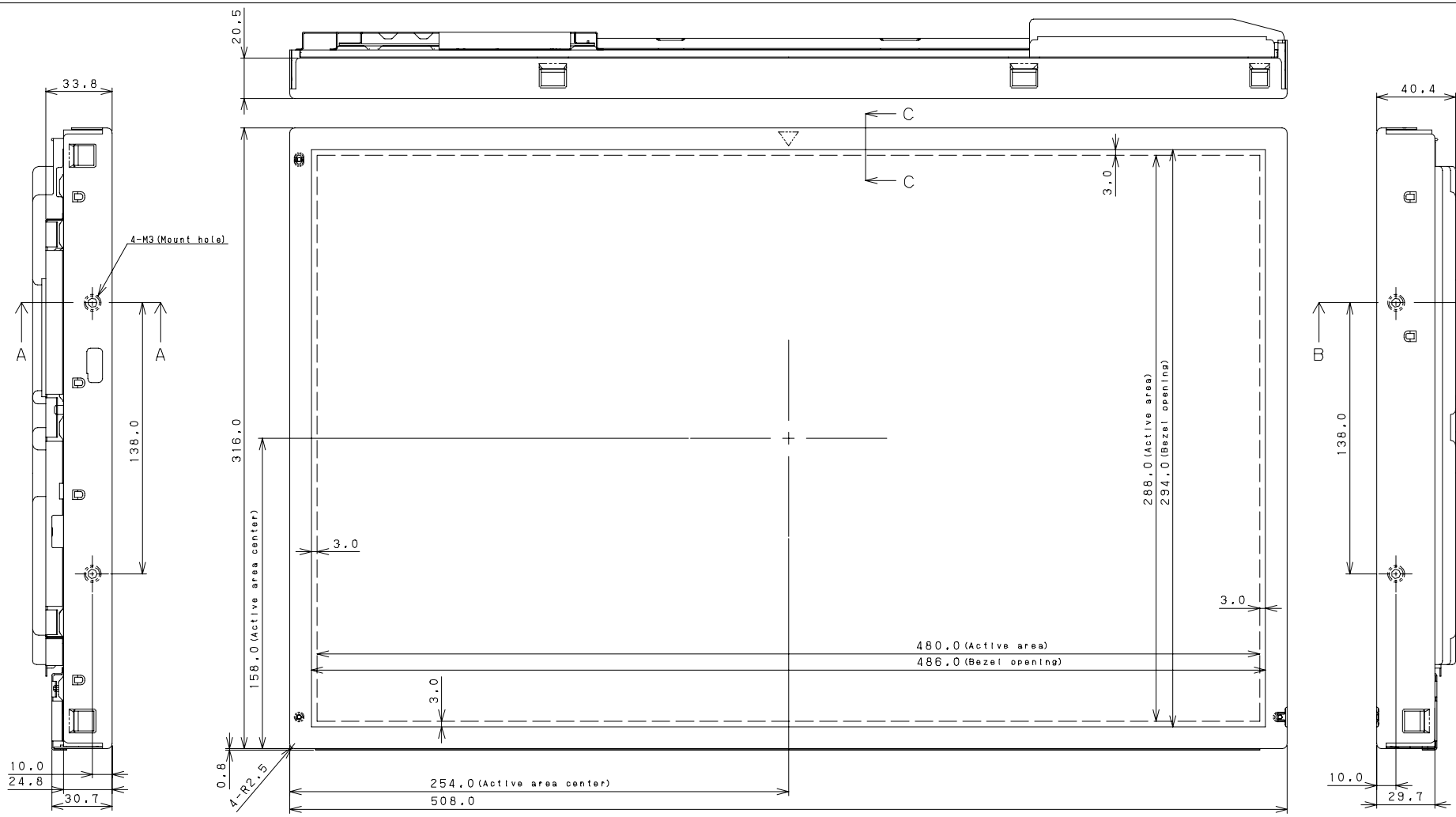
Specifications of the TFT-LCD panel and other components used in the LCD module are subject to change. Both parties shall discuss together before change.

If any doubt is raised in the content of the specifications, both parties shall discuss and make best effort for the agreement.

DOCUMENT CONTROL SECTION

DATE

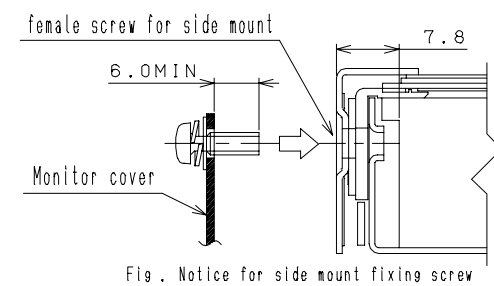
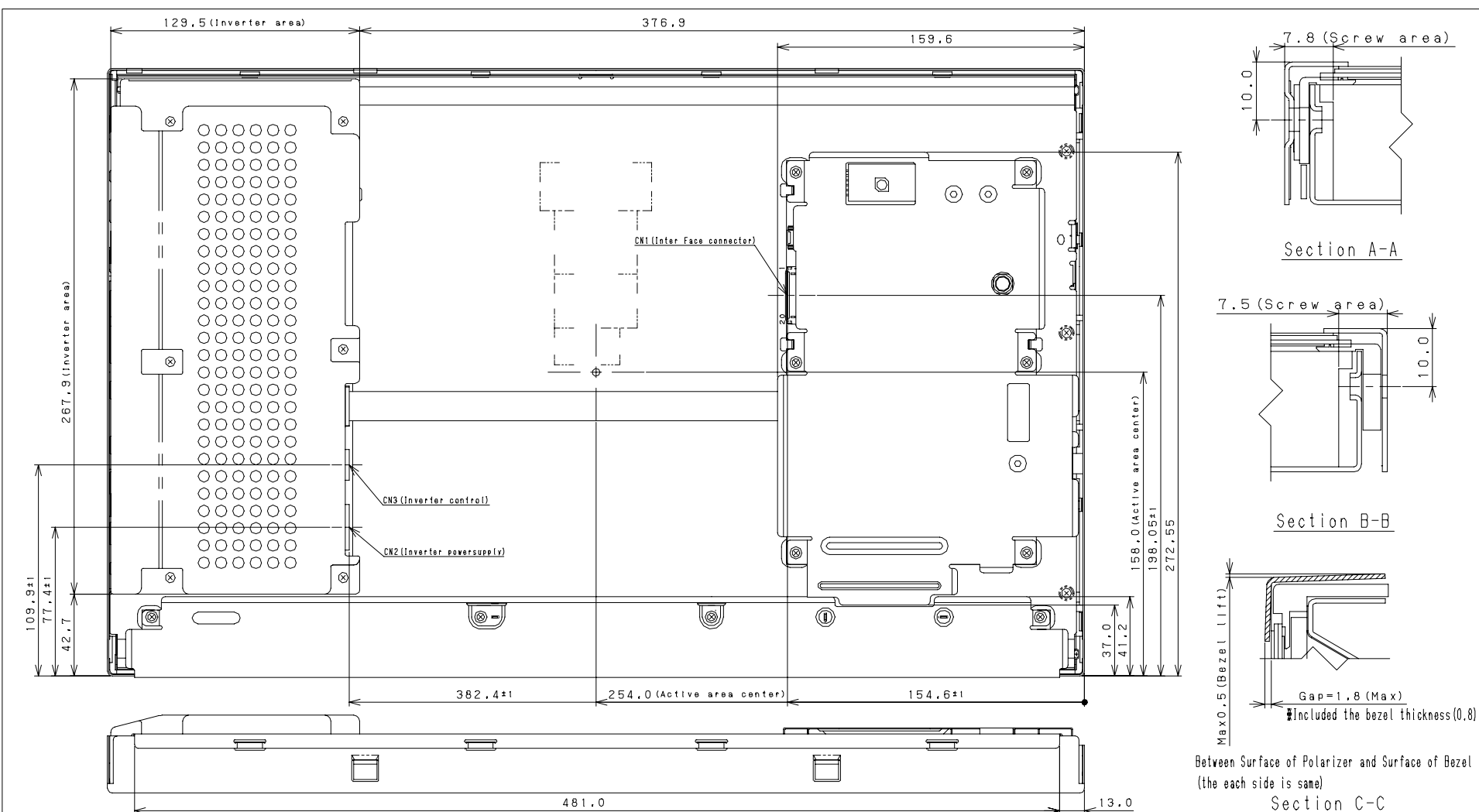
						FLC56XWC8V		
						Tech Bes LCD-00175		CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION			FUJITSU DISPLAY TECHNOLOGIES CORPORATION
DESIG.			CHECK		APPR.			27 /



**NOTE**

1) Unspecifild tollerance to be  $\pm 0.5$ .

MATER.		FINISH		TITLE		SCALE	
				FLC56XWC8V		1 : 2	
03 20030526		Total revision		DRAW. NO.		Tech Bes LCD-00175	
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION		DESIG.	APPR.
						28/	



Note1) Fixing screw of LCD module must have screw part length of more than 6.0mm with unfixing condition. Otherwise fixing screw may not be fixed screwed.

Note2) The length of the screw after fixing must be shorter than the screw area.

MATER.		FINISH		TITLE		SCALE	
				FLC56XWC8V		1 : 2	
03 20030526		Total revision		DRAW. NO.		Tech Bes LCD-00175	
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION		DESIG.	29/